

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	4443	((first cpu adj1 clock) and ((second bus) adj1 clock)	US-PGPUB	OR	ON	2006/08/31 07:53
L2	1074	1 and "71"\$.clas.	US-PGPUB	OR	ON	2006/08/31 07:53
L3	4057	1 and (controller control regulator)	US-PGPUB	OR	ON	2006/08/31 07:54
L4	96	3 and (predetermined adj1 timing)	US-PGPUB; USPAT	OR	ON	2006/08/31 07:54
L5	35	(cpu adj2 clock) with ((bus peripheral) adj2 clock)	US-PGPUB	OR	ON	2006/08/31 07:55
L6	4	5.clm.	US-PGPUB	OR	ON	2006/08/31 07:56
L7	15241	bus adj2 interface	US-PGPUB	OR	ON	2006/08/31 07:56
L8	135	(7 biu) with synchronous	US-PGPUB	OR	ON	2006/08/31 07:57
L9	28	8.clm.	US-PGPUB	OR	ON	2006/08/31 07:57
L10	5	(7 biu) with (predetermined adj1 timing)	US-PGPUB	OR	ON	2006/08/31 07:58
L11	9	biu near6 switch\$3	US-PGPUB	OR	ON	2006/08/31 07:58
L12	1	biu with (timing adj2 control)	US-PGPUB	OR	ON	2006/08/31 07:59
L13	481	((bus adj1 interface) or biu) near10 clock	US-PGPUB	OR	ON	2006/08/31 07:59
L14	64	13.clm.	US-PGPUB	OR	ON	2006/08/31 07:59
L15	5	((bus adj1 interface) or biu) near10 (cpu adj clock)	US-PGPUB	OR	ON	2006/08/31 08:00
L16	8202	micro adj controller	US-PGPUB	OR	ON	2006/08/31 08:00
L17	38	16 with (biu or (bus adj1 interface))	US-PGPUB	OR	ON	2006/08/31 08:00
L18	7	17.clm.	US-PGPUB	OR	ON	2006/08/31 08:01
L19	5549	first adj clock	US-PGPUB	OR	ON	2006/08/31 08:01
L20	1	(19 near6 coupled) with (biu or (bus adj1 interface))	US-PGPUB	OR	ON	2006/08/31 08:01
L21	6713	clock adj (supply supplier generator)	US-PGPUB	OR	ON	2006/08/31 08:02
L22	535	21 near12 switch\$3	US-PGPUB	OR	ON	2006/08/31 08:02
L23	44	22 same register	US-PGPUB	OR	ON	2006/08/31 08:02
L24	4	23.clm.	US-PGPUB	OR	ON	2006/08/31 08:09
L26	928	(713/600).CCLS.	US-PGPUB; USPAT	OR	OFF	2006/08/31 08:09